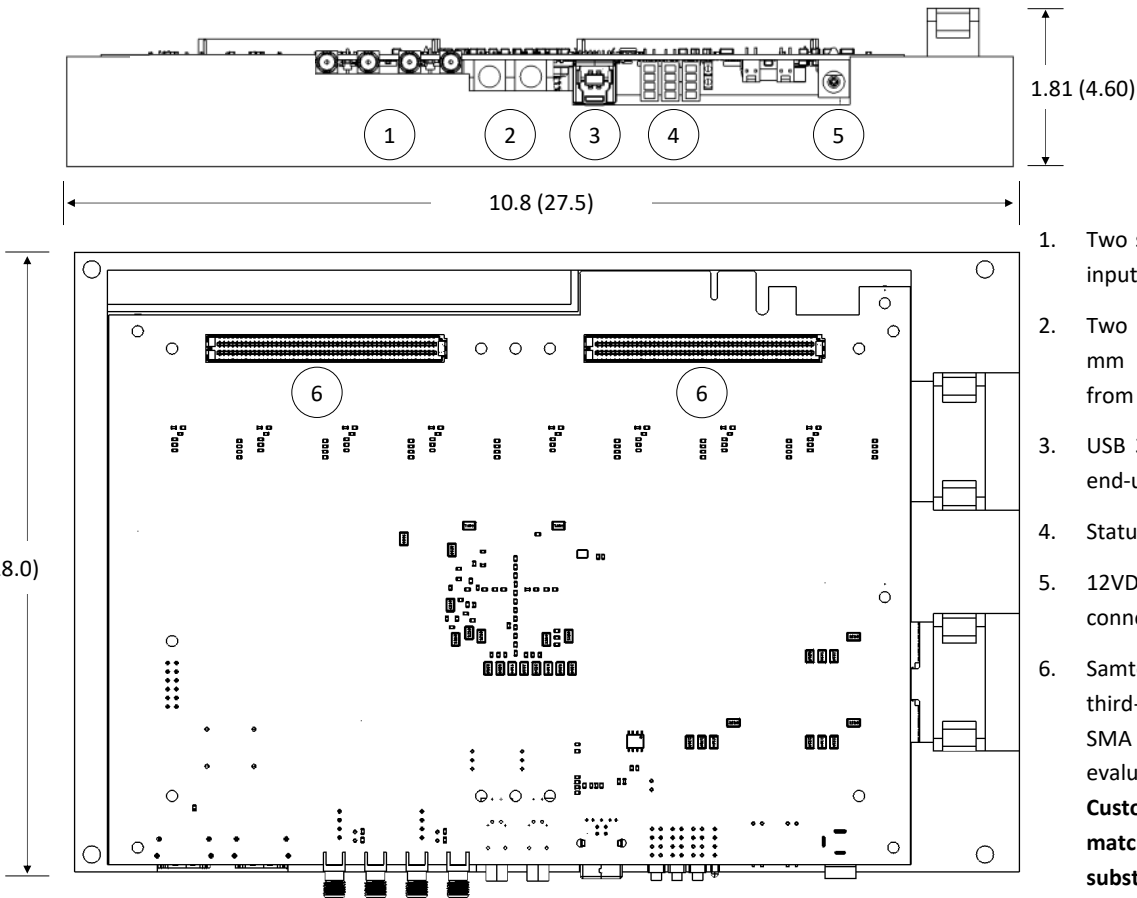


Programmable, High Channel Count Analog-to-Digital Converter (ADC)



- Compact housing and customizable input connectors for easy instrument integration
- Streaming ADCs for continuous data acquisition (no buffering), faster transmission and up to 50 fps
- Internal trigger generator allows external device triggering at defined frequencies. Continuous mode sends trigger signal as soon as previous acquisition is complete (highest frame rate)
- Integrated AFE amplifier chips with digitally controlled gain
- Optical and electrical trigger inputs
- Programmable Gain and SDK supports a wide range of data acquisition configurations
- Two optional breakout boards with 128 industry standard SMA connectors for evaluation, testing and development

Channels	Channels per ADC	256	
ADC	Programmable Gain	12 to 51 dB	(1) Low Pass programmable filters available
	Bandwidth @ -3 dB ⁽¹⁾	50 kHz to 12.5 MHz	(2) 38.5 MSPS default. Up to 40 MSPS with custom FPGA chip
	Resolution	12-bit	
	Sampling Rate ⁽²⁾	38 to 40 MSPS	(3) Per frame per channel
	Max Trigger / Frame Rate	50 Hz / fps	
	Max Points ⁽³⁾	4096	



All dimensions approximate in inches (cm).

Computer* (optional)	Software
4+ Core i7 Processor Nvidia Graphics Card for CUDA only 16+ GB DDR4 Memory 500+ GB PCIe Solid-State Drive Windows 10 64-bit	Windows 7/10 64-bit drivers Standalone DAQ Application Software Development Kit (LabView) *.tdms data output

* End-user or PhotoSound provided



Optional SMA inputs on a breakout board for unit evaluation, testing and development

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All specifications are subject to change without notice.

LEGION ADC256 is classified EAR99 and does not require an export license.

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