

Programmable High Channel Count Analog-to-Digital Converter (ADC) Unit with 256 Parallel Channels



- Compact housing and customizable input connectors for easy instrument integration
- Streaming ADCs for continuous data acquisition (no buffering) and faster transmission
- Internal trigger generator allows external device triggering at defined frequencies. Continuous mode sends trigger signal as soon as previous acquisition is complete (highest frame rate).
- Integrated amplifier chips with digitally controlled gain
- Optical and electrical trigger inputs
- Programmable Gain and SDK supports a wide range of data acquisition configurations
- Optional test and evaluation breakout boards with industry standard SMA connectors for all channels

ADC	Channels	Channels per ADC ⁽¹⁾	256	<p>(1) All channels fully parallel for simultaneous data acquisition without multiplexing (upgradeable to 1024 channels)</p> <p>(2) Depends on mode selection</p> <p>(3) Depends on mode/parameter selection (low pass programmable filters available)</p> <p>(4) Depends on PC specifications</p> <p>(5) Per frame per channel</p>
		Programmable Gain ⁽²⁾	6 to 51 dB	
		Analog Bandwidth @ -6 dB ⁽³⁾	12.5 kHz to 25 MHz	
		Resolution	12-bit	
		Sampling Rate	40 MSPS	
		Max Trigger / Frame Rate ⁽⁴⁾	200 Hz / fps	
		Max Points ⁽⁵⁾	4096	



All dimensions approximate in inches (cm).

Computer* (optional)	Software
4+ generation Core i7 Processor Nvidia Graphics Card for CUDA only 16+ GB DDR4 Memory 500+ GB PCIe Solid-State Drive Windows 10 64-bit	Windows 7/10 64-bit drivers Standalone DAQ Application Software Development Kit (LabView) TDMS data output

* End-user or PhotoSound provided



Optional SMA inputs on a breakout board for unit evaluation, testing and development

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